FIG. I(a)

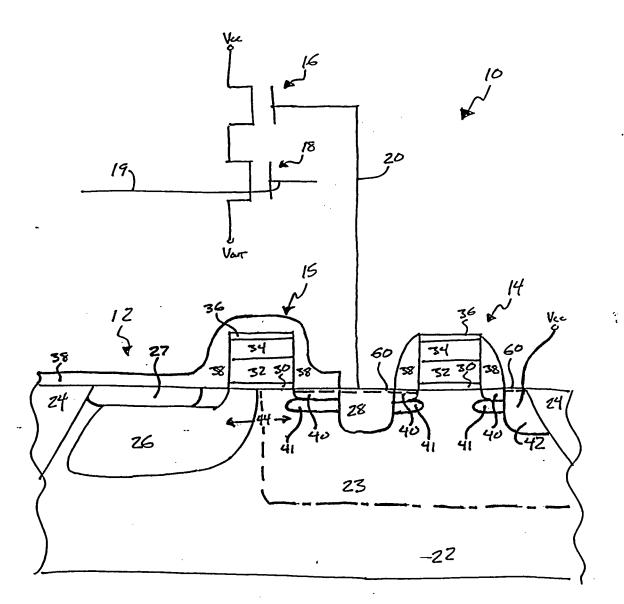


FIG. 1(b)

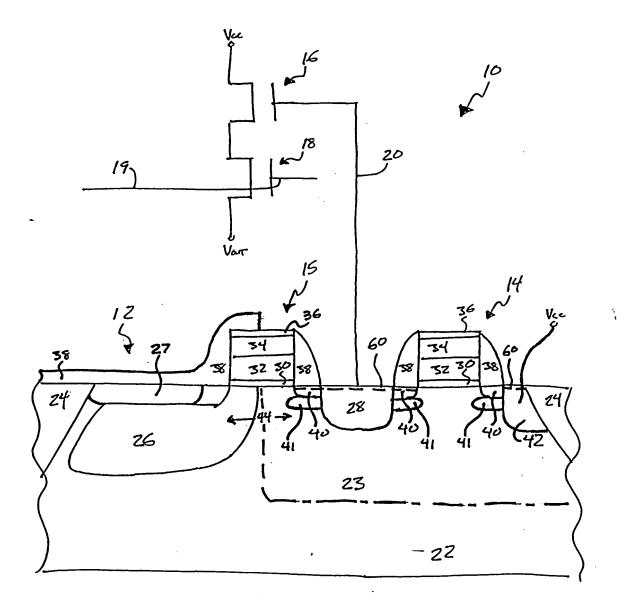
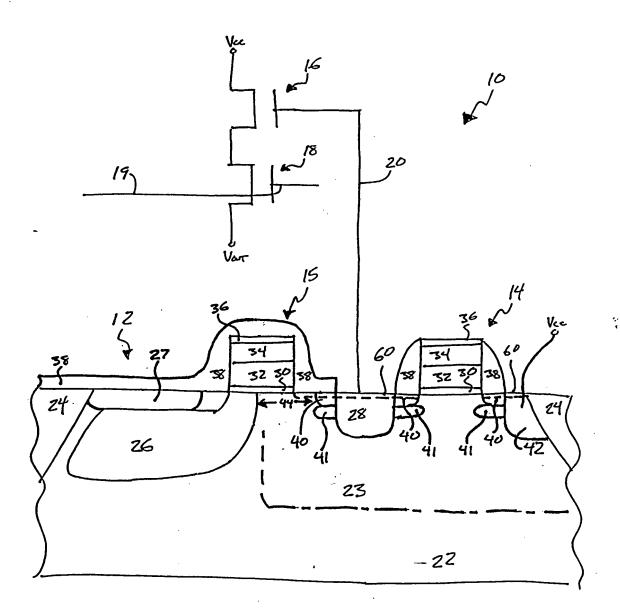
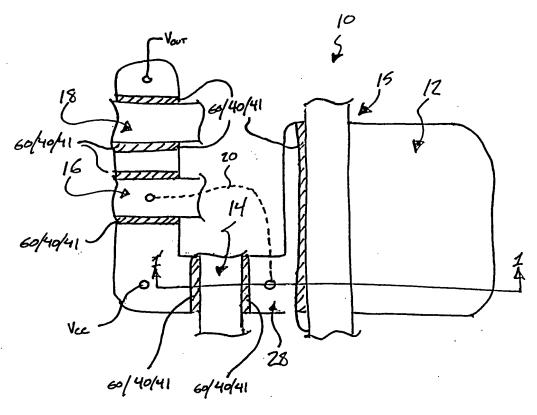
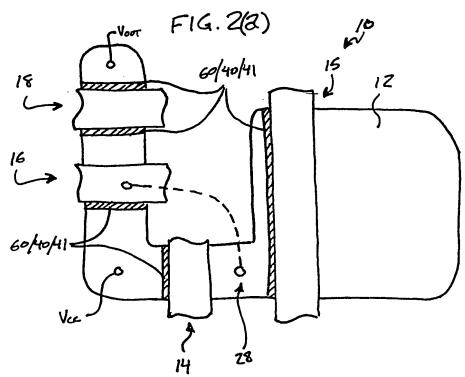


FIG. 1(c)



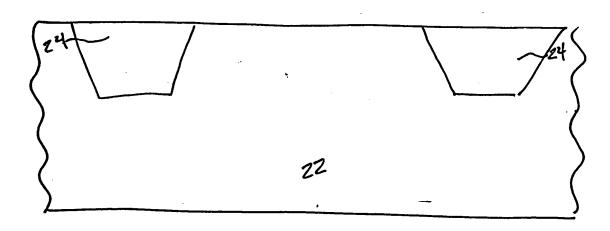
F14. Z



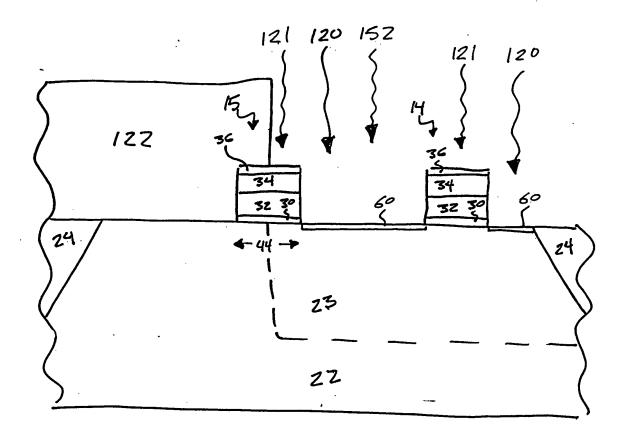


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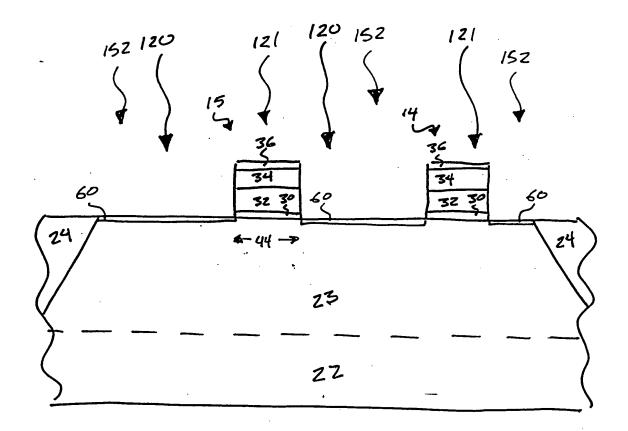
F14.3



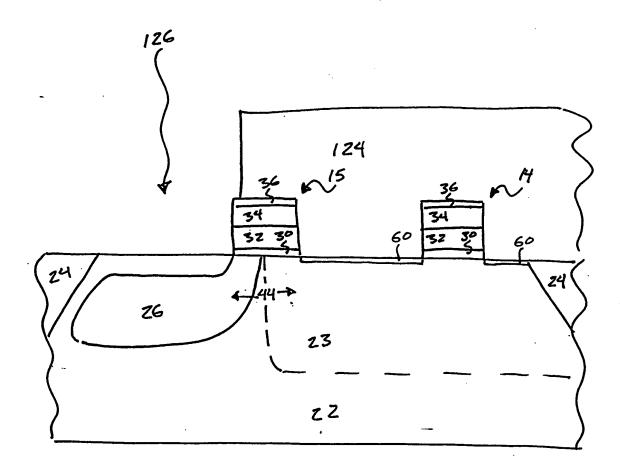
F164 4(2)



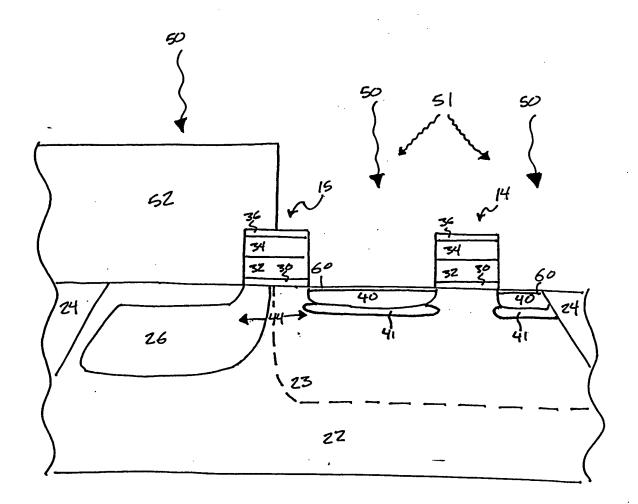
F16 4(b)



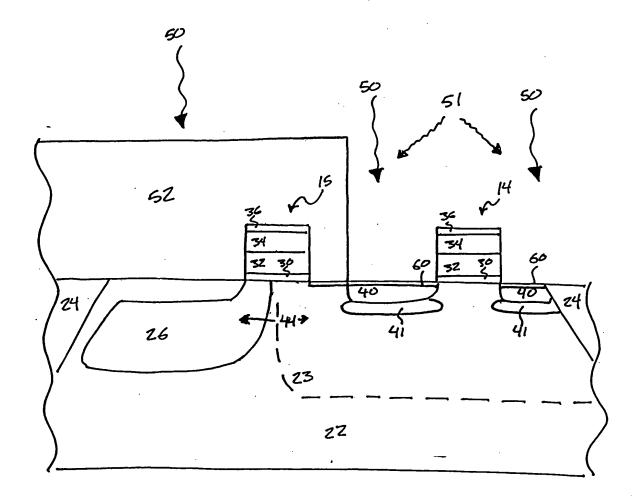
F14.5



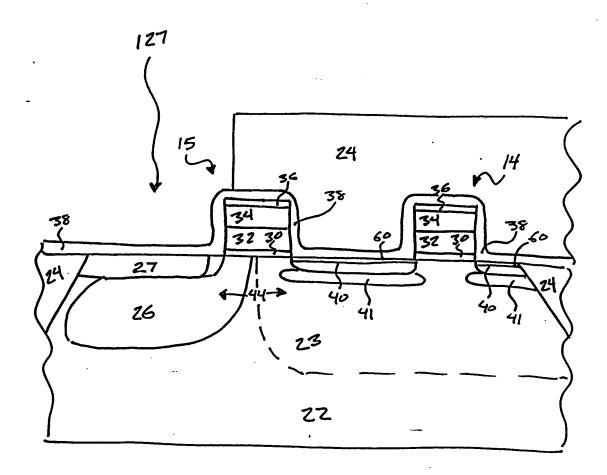
F14.6(æ)



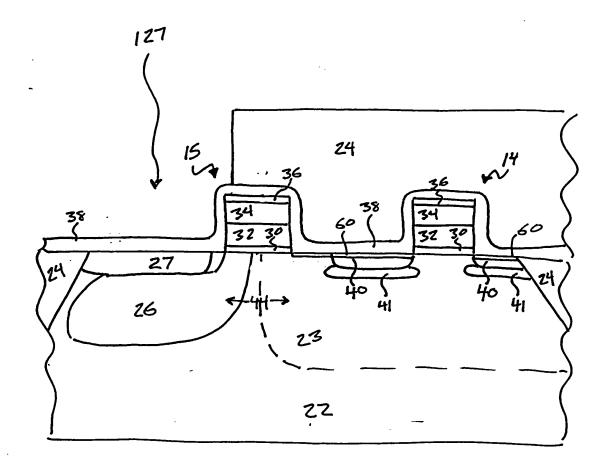
F14.6(b)



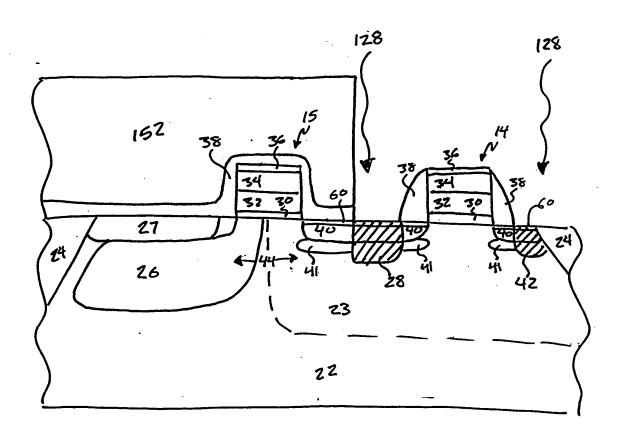
F1a. 7(2)



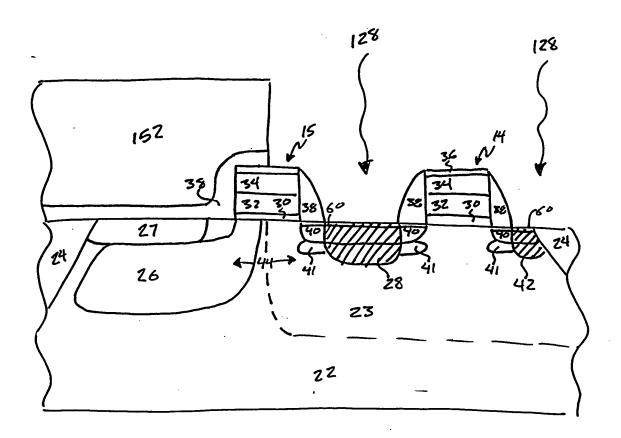
F16.7(b)



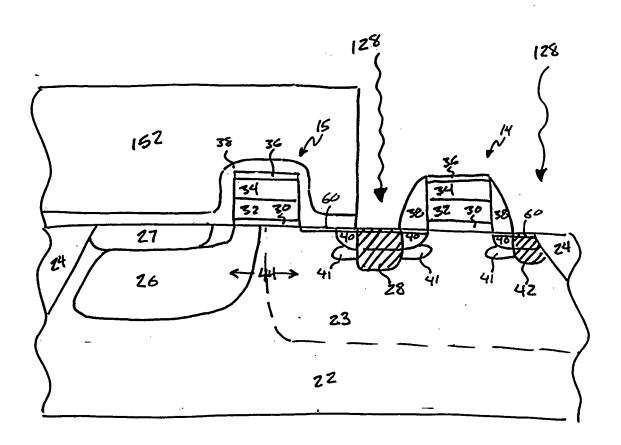
F14. 8(2)

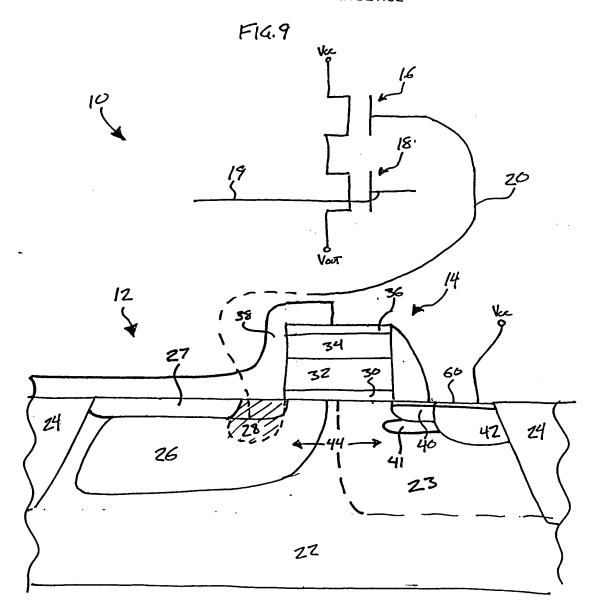


F14. 8(b)

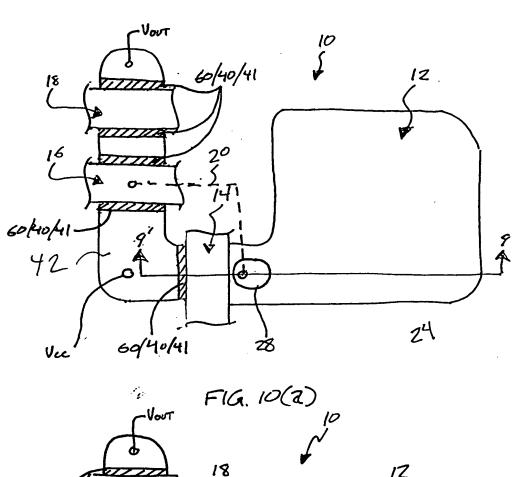


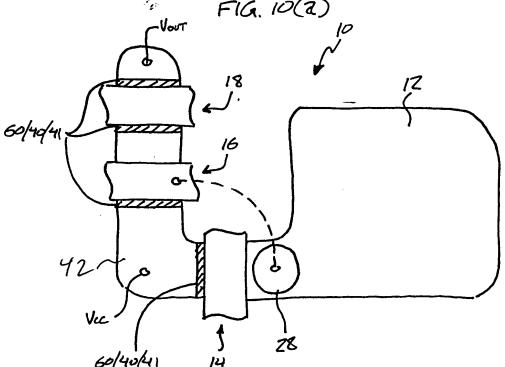
F14.8(c)



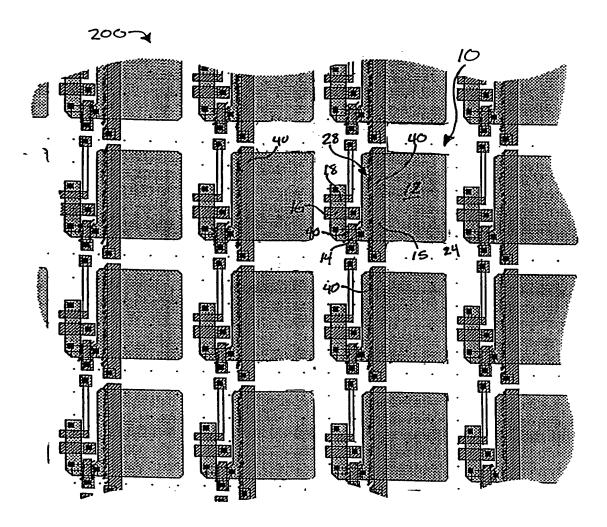


F1410

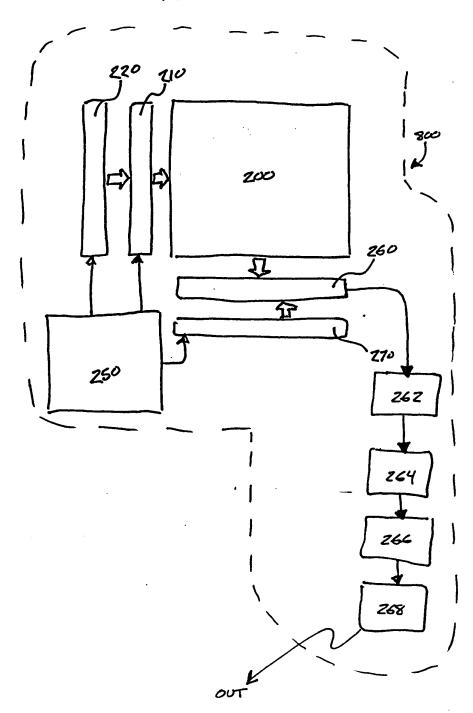




F14.11



F14.12



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